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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant: Bryan D. Sheffield, et al.

Docket Number: TI-35286

Serial No.: 10/663,144

Art Unit: 2185

Filed: 09/16/03

Examiner: Daniel Y. Kim

For: Method of Operating a Memory at High Speed Using a
Cycle Ready Status Output Signal

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| NAME OF INVENTOR(S): Bryan D. Sheffield, et al. | |
| RECEIPT DATE & SERIAL NO.: Serial No.: 10/663,144 | |
| TITLE OF INVENTION: Method of Operating a Memory at High Speed Using a Cycle Ready Status Output Signal | |
| Filing Date: 09/16/03 | |
| TI FILE NO.: TI-35286 | DEPOSIT ACCT. NO.: 20-0668 |
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Applicant: Bryan D. Sheffield, et al. Art Unit: 2185
Serial No.: 10/663,144 Examiner: Daniel Y. Kim
Filed: 09/16/03 Docket: TI-35286
For: METHOD OF OPERATING A MEMORY AT HIGH SPEED USING A CYCLE
READY STATUS OUTPUT SIGNAL

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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AMENDMENT

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